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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.								
10/528,474	03/21/2005	Chee Yen Tee	SG02 0024 US	4373								
65913 NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131	7550 12/24/2008		<div>EXAMINER</div> <div>PATEL, NIKETA I</div> <table border="1"><thead><tr><th>ART UNIT</th><th>PAPER NUMBER</th></tr></thead><tbody><tr><td>2181</td><td></td></tr></tbody></table> <div><table border="1"><thead><tr><th>NOTIFICATION DATE</th><th>DELIVERY MODE</th></tr></thead><tbody><tr><td>12/24/2008</td><td>ELECTRONIC</td></tr></tbody></table></div>		ART UNIT	PAPER NUMBER	2181		NOTIFICATION DATE	DELIVERY MODE	12/24/2008	ELECTRONIC
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**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

# Office Action Summary

## Application No.

10/528,474

## Applicant(s)

TEE ET AL.

## Examiner

NIKETA I. PATEL

## Art Unit

2181

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 07 August 2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-15 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07 August 2008 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-8508)
- \_\_\_\_\_ Paper No(s)/Mail Date \_\_\_\_\_

- 4) ☐ Interview Summary (PTO-413)
- \_\_\_\_\_ Paper No(s)/Mail Date \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_

## DETAILED ACTION

### *Response to Arguments*

1. Applicant's arguments with respect to the § 102(e) rejection of claims 1-15 have been considered but are moot in view of the new ground(s) of rejection.

### *Specification*

2. Applicant is **kindly requested** to follow the guidelines provided in 37 CFR 1.77(b).

The following guidelines illustrate the preferred layout for the specification of a utility application. These guidelines are suggested for the applicant's use.

### **Arrangement of the Specification**

As provided in 37 CFR 1.77(b), the specification of a utility application should include the following sections in order. Each of the lettered items should appear in upper case, without underlining or bold type, as a section heading. If no text follows the section heading, the phrase "Not Applicable" should follow the section heading:

- (a) TITLE OF THE INVENTION.
- (b) CROSS-REFERENCE TO RELATED APPLICATIONS.
- (c) STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR DEVELOPMENT.
- (d) THE NAMES OF THE PARTIES TO A JOINT RESEARCH AGREEMENT.
- (e) INCORPORATION-BY-REFERENCE OF MATERIAL SUBMITTED ON A COMPACT DISC.
- (f) BACKGROUND OF THE INVENTION.
  - (1) Field of the Invention.
  - (2) Description of Related Art including information disclosed under 37 CFR 1.97 and 1.98.
- (g) BRIEF SUMMARY OF THE INVENTION.
- (h) BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING(S).
- (i) DETAILED DESCRIPTION OF THE INVENTION.
- (j) CLAIM OR CLAIMS (commencing on a separate sheet).
- (k) ABSTRACT OF THE DISCLOSURE (commencing on a separate sheet).
- (l) SEQUENCE LISTING (See MPEP § 2424 and 37 CFR 1.821-1.825. A "Sequence Listing" is required on paper if the application discloses a nucleotide or amino acid sequence as defined in 37 CFR 1.821(a) and if

the required "Sequence Listing" is not submitted as an electronic document on compact disc).

***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

4. Claims 1-15 are rejected under 35 U.S.C. 102(a) as being anticipated by TransDimension OTG243 Product Brief (hereinafter "TransDimension".)

5. Referring to claims 1, 3, 5, 6, taking claim 1 as exemplary, TransDimension teaches an interface integrated circuit device for interfacing a USB connection to a further circuit, the interface integrated circuit comprising: first external terminals for connecting to a USB bus [figure 1, element "USB transceiver" and page 1, column 1]; a transceiver capable of transceiving for both a USB host and a USB device [figure 1, element "OTG transceiver"], the transceiver having a USB interface [figure 1, element "Function SIE", multiplexer, "OTG transceiver; and page 1, column 1], a host interface and a device interface, the USB interface being coupled to the first external terminals [figure 1, element "Host SIE & Root Hub", multiplexer, "OTG transceiver; and page 1, column 1]; second external terminals coupled to the device interface for connection to an external USB device controller [figure 1, element "USB transceiver"]; a host controller coupled to the host interface [figure 1, element "USB Host Control Logic", "Host SIE & Root Hub"], the host controller having a parallel data/address bus [figure 1,

element "Microprocessor interface", "USB Host Control Logic" and page 3, description of "microprocessor interface"]; third external terminals coupled to the parallel data/address bus [figure 1, element "Microprocessor interface" and pages 1-4.]

6. Referring to claim 2, TransDimension teaches an integrated circuit device according to claim 1, wherein the device interface comprises both an analog USB device interface and a transceived digital USB device interface for connection to an external USB device controller without and with an external transceiver, respectively [figure 1 and pages 1-4.]

7. Referring to claim 4, TransDimension teaches an electronic apparatus according to claim 3, wherein the apparatus is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as USB device [figure 1 and pages 1-4.]

8. Referring to claim 7, TransDimension teaches a method according to claim 6, wherein passing USB signals from the transceiver to the device controller in the functional circuits outside the integrated circuit includes passing analog USB signals from the transceiver to the device controller when the device controller includes a transceiver and passing transceived digital USB signals from the transceiver to the device controller when the device controller does not include a transceiver [figure 1 and pages 1-4.]

9. Referring to claim 8, TransDimension teaches a method according to claim 6, wherein the interface integrated circuit does not include a USB device controller [figure 1 and pages 1-4.]
10. Referring to claim 9, TransDimension teaches an interface integrated circuit device according to claim 1, wherein the interface integrated circuit does not include a USB device controller [figure 1 and pages 1-4.]
11. Referring to claim 10, TransDimension teaches an interface integrated circuit device according to claim 1, wherein the interface integrated circuit is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as a USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as a USB device [figure 1 and pages 1-4.]
12. Referring to claim 11, TransDimension teaches an electronic apparatus according to claim 3, wherein the device interface includes both an analog USB device interface and a transceivd digital USB device interface for connection to the USB device controller circuit without and with an external transceiver, respectively [figure 1 and pages 1-4.]
13. Referring to claim 12, TransDimension teaches an electronic apparatus according to claim 3, wherein the interface integrated circuit does not include a USB device controller [figure 1 and pages 1-4.]

14. Referring to claim 13, TransDimension teaches an electronic system according to claim 5, wherein the device interface includes both an analog USB device interface and a transceived digital USB device interface for connection to the USB device controller circuit without and with an external transceiver, respectively [figure 1 and pages 1-4.]

15. Referring to claim 14, TransDimension teaches an electronic system according to claim 5, wherein the interface integrated circuit does not include a USB device controller [figure 1 and pages 1-4.]

16. Referring to claim 15, TransDimension teaches an electronic system according to claim 5, wherein the system is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as a USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as a USB device [figure 1 and pages 1-4; full speed and low speed.]

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to NIKETA I. PATEL whose telephone number is (571)272-4156. The examiner can normally be reached on M-F 8:00 A.M. to 6:00 P.M, with every other Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alford Kindred can be reached on (571) 272 4037. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Niketa I. Patel/  
Primary Examiner, Art Unit 2181